PX1

Tile-Side Processing

Harmonize your display with Megapixel's PX1, a tile-side processor for HELIOS®-compatible tiles. PX1 processes video, color, and real-time metadata to create a perfect unison across your entire display and across multiple processors.

Up to 78-bit Greyscale

Large Pixel Capacity

Full HDR Support

Advanced Color Management

megapixel™

SPECIFICATIONS

PART NUMBER	PCB-0016
PROCESSING	230k pixels nominal capacity per card Up to 26-bit per color / 78-bit per pixel using Megapixel's proprietary LightScience™ technology RGB+N support for up to 6 LED pixel primaries
OUTPUTS	Customizable to most driver schemes Up to 64:1 scan multiplexing
NETWORK	Up to 4 x 1G/2.5G Ethernet interfaces Redundancy support through loop-back using 2x interface links or a mesh network using 4x interface links
SENSORS & UI SUPPORT	I2C/SPI sensors RGB tile status LED Push-button for recalling internal test patterns
CALIBRATION	Data stored/retrieved from LED display modules Optional internal calibration storage Full gamut color correction
ADVANCED COLOR MANAGEMENT	On-the-fly gamut reshaping and calibration re-targeting without storing multiple calibration banks Advanced Y-RGB tone-mapping for HDR support
MECHANICAL	SODIMM DDR2 form factor 67.6mm x 47mm

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